

Notice of References Cited	Application/Control No. 09/844,673	Applicant(s)/Patent Under Reexamination THEKKATH, RADHIKA	
	Examiner J. Derek Rutten	Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,526,504 A	06-1996	Hsu et al.	711/207
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"MIPS Technologies introduces world's first synthesizable 64-Bit Processor Core", 12/22/04, D&R Headline News, accessed and printed online at < http://www.us.design-reuse.com/news/news2023.html >
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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